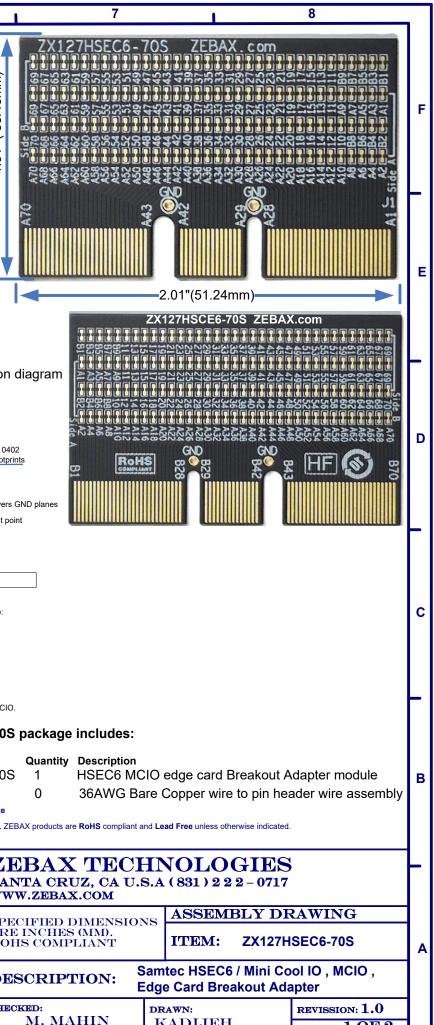
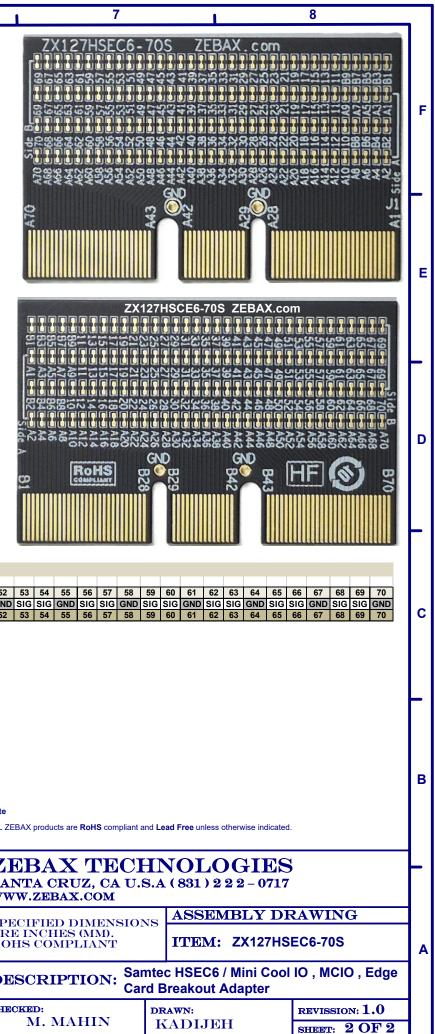
	1		1	2	1	3	1		4	1	5	1	6	
	Product Name	e: ZX127	HSEC6-70	S Samtec	GENERAT	E HSEC6 / Mi	ini Cool Edg	ge IO , MCI	O, edge car	d breakou	t adapter –	Page 1 OF	2	-
F			X127HSEC6-			6 0.6mm pitch als ne electrical test			-		-			1.31" (33.46mm)
						pporting Samtec SEC6 / MCIO con								1.31"
E	2- The s 3- Listed 4- All tra 5- Four, 6- Acce 7- Ease 8- Reas	shunts are s d number ac aces are imp , 4, layers Po essible GND e of interface ssign any sig	tandard 0402 ljacent to eac bedance contr CB design, inr test point, The with single ch	SMD footprin h Shunt is in r olled. her layers are e test point is hannel and dif the shunts' ir	t package size reference to the GND planes. connected to ferential scopt oner connectir	he Samtec HSEC inner GND plane	06 / MCIO conn			of PCB for ea	asy probe acco	ess.		
		-				ZX00BC2PH1					Z	X127HSEC6-	70S simplified	d cross sectio
D	Electrical: Insertion loss > -2dB @6GHz Trace impedance: 50 Ω Operating Temperature: -55°C to +125°C Edge Card Connector:					ZX127HSEC6-70S simplified circuit					uit diagram			
_	Shunt: ZX00BC Application:	Pitch: 0 Thicknes Plating: Package C2PH1 : 36A	rith: Samtec H .60mm pin to ss: 1.6mm (0 10μ" (0.25μr e: Standard 0 .WG Bare cop uring test mea	pin pitch).062") n) 402 SMD pac oper wire to pi	kage n header	ch connector p, testing , debug	aina		4 1 3 1 1 1 1 1 1 1 1 1 1		2 SMD Package	Shunts- 0402 SMD footprints Samtec H Mini Cool	SEC6 Plug IO Plug Most orboard HSEC6 co Mn CcP4 IO. MCIO HOST PCB	Shunts- SMD foc
с	Mates with :	Samtec G	GENERATE H	igh Speed Ed	lge Card HSE	EC6 & GC6 cable	assembly		Camtec HSEC6 Plug	J1	0	Note: Ground layers, Pleas 1- Top/Bottom GN 2- The inner layers		t point is connector to
_		GEN-Z 1C MET0056	C, Amphenol C 10101011 ME 11202041 ME	697, GH01 se 10056102030	ries. ACES E 071 ME10056	F-TA-1016, SFF- lectronics, LOTE 610201091 ME10 610205081 ME1	S 005611201041	Note: 1- 2- Shunts	Mirli Cool IO,	es of ZX127HSEC6-7		2- Shunts are avai module, making ALL accessible on both s	dard 0402 SMD packag lable on both sides of Z HSEC / MCIO connect	X127HSEC6-70S tor's signals are
в	Industry Stan	SFF-TA_1	002, SFF-TA	-1016, PCI Ex	press Gen 3.	adopted by indust .0, 4.0, 5.0, 6.0, C Cle , NVMe , SAS	OCP DC-MHS ((HSIO recom	nmended)	IS(Ro EL	ompliance: D2001 certified Hs - Lead Free J RoHS2		Part ZX1	27HSEC6-70 number 27HSEC6-70 00BC2PH1
_	Access: For signal meas 1- Reco		n: Use 36AW0	G solid coppe	r wire with pin	n header,ZX00BC	2PH1 or simila	ar		EL Eu Ha Ro	. E111594 docum V- Vehicle Directi Iropean Union Dir Ilogen Free per IE Hs Directive 2011 EEE Directive (20	ve (Directive 20 ective (203/11/E C-61249-2.21 : 2 I/65/EU	C)	Not ALL
Α	1- Cut the inner connecting trace within any Shunt and reassign to new location. Certificate of Com 2- Using 36AWG solid copper wire, make the required connections. See Signal Access & re-route, Page 2 Certificate REACH							ertificate of Complientificate of Complientificate of Complientificate REACH S	ompliance for Radioactive substances ompliance for Asbestos ompliance for Ozone Depleting Substances, ODS ICH SVHC ompliance RoHS_EN_CoC					
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	IMPLIED, STATUTORY, OR O Information furnished is believe Specifications mentioned in this	ed to be accurate an	d reliable. However, Zo	ebax Technologies ass	sumes no responsibility	y for the consequences of us	se of such information or	r for any infringement	of patents or other rights	of third parties that m				CH
									-					



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PECIFIED DIMENSIONS	ASSEMBLY DRAWING								
REINCHES (MM). OHS COMPLIANT	ITEM: ZX127HSEC6-70S								
DESCRIPTION: Samtec HSEC6 / Mini Cool IO , MCIO , Edge Card Breakout Adapter									
	rawn: KADIJEH	REVISSION: 1.0 SHEET: 1 OF 2							
7		8							

	1	2	3		4	1	5		6	
	Product Name:	ZX127HSEC6-70S Sa	mtec GENERATE HSEC6 /	Mini Cool E	dge IO,MCIO,	card breakc	out adapter – P	age 2 of 2		
F	Signal Access & re-ro		l on ZX127HSEC6-70S is impleme direct to the new signal or test equip	• •	inner-connecting					
_	GND tes	t point as well as top & botto	lesign where the inner layers are G om GND fills. For improved signal in Cross Section diagram for details.	•	-					
Е	Typical Application:	approach in usage of break 1- Utilizing single or differe	ential scope probe. s, by re-assignment of any signal b			-				
_	2- Zebax	commended to keep the pro	be wire length at 0.5" (1.2cm) long 36AWG Bare Copper wire to pin he al probe.	-	mbly) is an ideal pr	obing wire solu	ution where the hea	ader fits		
D			lems, please use the shortest Grou test point to be utilized as GND ref			arest GND refe	erence point.			
_	SFF-TA-1002 3C (2 rows x 70 pins/row) 140 pins standard- Below are listed signals and Ground pins assignments for the SFF-TA-1002 standard, please refer to the standard for formal signals map naming. This table denotes "SIG" or "GND" PIN geometry locations. The Grounds are not bussed together in the connector or the ZX127HSEC-70S breakout adapter. The listed GND signals are routed similar to the SIG signals, they are all individually routed signals.									
	ZX127HSEC-70S Pin Geometry Pa		rd 15 16 17 18 19 20 21 22 23 24	4 25 26 27 28	29 30 31 32 3	3 34 35 36 37	7 38 39 40 41 42	43 44 45 46	6 47 48 49 50 51 52	
с	NameGNDSIGSIGGNDSIGSIGSideB123456		SIG GND SIG SIG GND SIG SIG <th></th> <th>GND SIG SIG GND SI</th> <th>IG SIG GND SIG SI</th> <th>G GND SIG SIG GND SIG</th> <th></th> <th>ND SIG SIG GND SIG SIG GND 6 47 48 49 50 51 52</th>		GND SIG SIG GND SI	IG SIG GND SIG SI	G GND SIG SIG GND SIG		ND SIG SIG GND SIG SIG GND 6 47 48 49 50 51 52	
	HSEC6 / MCIO footpri	nt: ZX127HSEC6-70S mate	es with industry standard footprint fo	or SFF-TA-100	2 1C(2rows x 70 p	ins per row)1	40 pins connectors			
в										
									Note ALL ZE	
Α									SPH ARI ROL	
			ND OTHER DOCUMENTS (TOGETHER AND SEPARATE						DE	
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	1	2	3		4	I	5	I	6	



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