Product Name: **ZX104LNR-38- Receptacle Mictor Breakout Adapter**

Product Description: Mictor breakout adapter Receptacle 38 pin Tyco/AMP/TE. Designed in 4 layer PCBoard with improved signal integrity and crosstalk. ZX104LNR is designed for general Mictor application use as well as ***Characterization***. All data line traces Even / Odd match associated CLK (Even / ODD) trace length at 50 Ω for slew, timing and characterization (PVT Variation of Load [**P**ower], ASIC supply Power **V**oltage, and ASIC **T**emperature.

In addition to ALL Mictor signal being available at header, ***dedicated*** test points for I2C-SDA, I2C-SCL, +5V, GND is provided for direct connection

"**GND"** Header test points are connected to 2 inner layer GND plates ***along with Mictor's center GND tap.*** Please use this GND as your system GND reference.

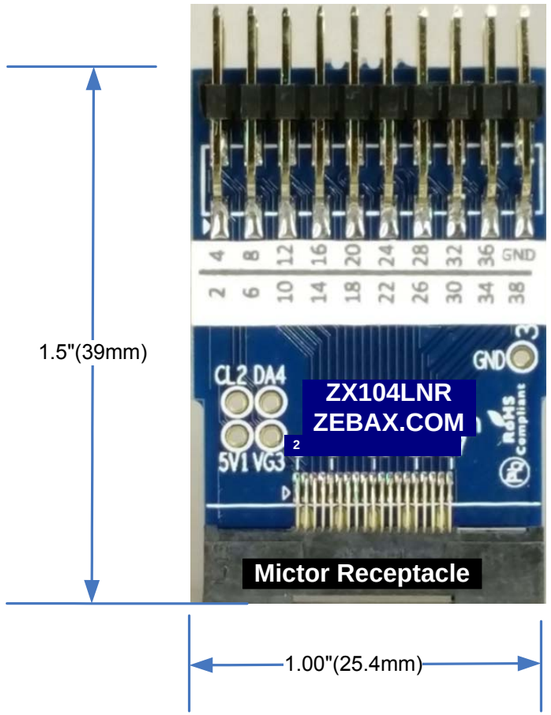
Application: Bringup, testing, emulation, development, modular design evaluations, DDR qualification

Mates with : Any Tyco's 38 pin Mictor Plug offered at any height
767007-8 767056-1 5767007-8 5767056-1
767139-1 767140-1 5767139-1 5767140-1
767003-9 767087-1 5767003-9 5767087-1
767005-8 767057-1 5767057-1 5767057-1
767025-1 5767025-1 767025-2 5767025-2
767042-1 5767042-1 767042-2 5767042-2
767032-1 5767032-1 767032-2 5767032-2
767017-1 5767017-1 767017-2 5767017-2
767006-1 5767006-1 767055-1 5767055-1

Agilent 5346-6002 E5339A E5334A E5351A E5346-6002 E5346-63201

Pitch: 0.64mm (0.025") centerline contact spacing connector, Legacy Mictor High Speed connector

Headers: 0.1" (2.54mm) center, 0.025" SQ with 0.228" (5.8mm) post height



ZX104LNR- Mictor 38 Receptacle pin configuration

Layer

TOP	**J2**	**4**	**8**	**12**	**16**	**20**	**24**	**28**	**32**	**36**	**GND**
		2	**6**	**10**	**14**	**18**	**22**	**26**	**30**	**34**	**38**

TOP	**Mictor_Receptacle**	**J1**	**2, 4, 6, 8, 10, .. 38**
BOTTOM			**1, 3, 5, 7, 9, .. 37**

BOTTOM	**J3**	**3**	**7**	**11**	**15**	**19**	**23**	**27**	**31**	**35**	**GND**
		1	**5**	**9**	**13**	**17**	**21**	**25**	**29**	**33**	**37**

Header numbering refers to the Mictor's pin number
"GND" header pins are connected to the 2 internal ground layers as well as top/bottom GND fills.

Test Point	Mictor pin #	Duplicate pin # at header	Function*
DA 4	4	SDA/4	I2C Data
CL2	2	SCL/2	I2C Clock
VG3	3	VG/3	Ground
5V1	1	5V/1	Supply
GND	center TAP	GND	

"*" Reserved specific board design function, not applicable to all designs.

GND TP is connected to Mictor's GND center tap as well as 2 internal ground planes.

Note
ALL ZEBAX products are **RoHS** compliant and **Lead Free** unless otherwise indicated.

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SPECIFIED DIMENSIONS ARE INCHES (MM).
ROHS COMPLIANT

ASSEMBLY DRAWING

ITEM: ZX104LNR-38-Receptacle

DESCRIPTION: Mictor breakout adapter 38 pin Receptacle

CHECKED: M. MARINA
DRAWN: SLAVIK
REVISSION: 1.0
SHEET: 1 OF 1