

**NEWS LETTER**

**Altera High Speed Mezzanine Card , HSMC breakout adapter, Samtec QSH QTH 090 breakout adapter**

**[Altera High Speed Mezzanine card, HSMC \( Breakout Adapter \)](#)**

Zebax offers HSMC mezzanine cards, test board and breakout adapters tailored for Altera High Speed Mezzanine Card, HSMC, bus interface test and any design using Samtec QTH or QSH connector. ZX106x, ZX107x modules are in compliance with Altera HSMC guidelines meeting 50 Ohms trace impedance. The ZX106x, ZX107x modules are designed for purpose of pre-bringup, bringup, test, debug and characterization applications.

**Application:** Functional and interface testing , test debug bringup characterization and pre-bringup.

**KEYSIGHT - Agilent Test Instrument division was changed to KEYSIGHT.**

**[ZX107HSMC-S](#)**

Introducing [ZX107HSMC-S](#) designed for test/debugging of Altera HSMC bus interface. All HSMC signals are accessible available on Agilent KEYSIGHT Logic Analyzer probe, E5378A, E5379A, E5385A using Samtec connector— Additionally; [ZX132](#) breakout adapter may be used to access individual signals. Onboard IPEX connectors, enable access clocks to/from Host-daughter card. Designed in 6 layers 100 Ω differential trace impedance.

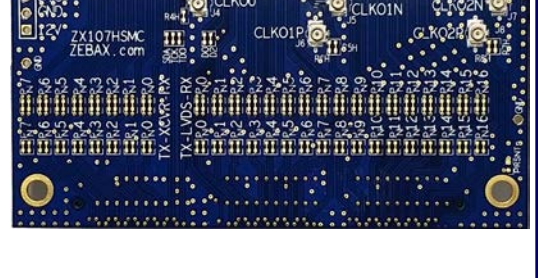
**Application:** Pre-Bringup, bringup, test, debug of Altera HSMC bus interface.



**[ZX107HSMC](#)**

[ZX107HSMC](#) designed for test/debugging of Altera HSMC bus interface. All HSMC signals are accessible available on 0402 SMD landing pads. Onboard IPEX connectors, enable access clocks to/from Host-daughter card. Designed in 6 layers 100 Ω differential trace impedance.

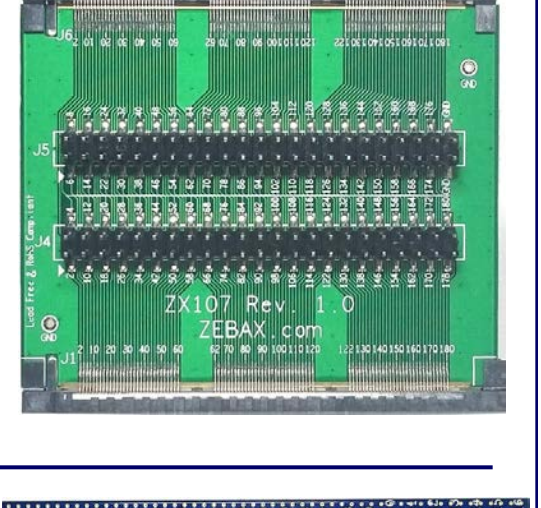
**Application:** Pre-Bringup, bringup, test, debug of Altera HSMC bus interface.



**[ZX107-QSHQTH090](#)**

[ZX107-QSHQTH090](#) is general purpose breakout adapter designed for pre-bringup, and testing of any design using Samtec QSH QTH or Alter HSMC connectors. [ZX107-QSHQTH090](#) may be placed between host and daughter card ( mezzanine card ) . All Samtec QTH / QSH connector signals are accessible via 0.1” pitch header. Designed in 4 layers with 50Ω trace impedance.

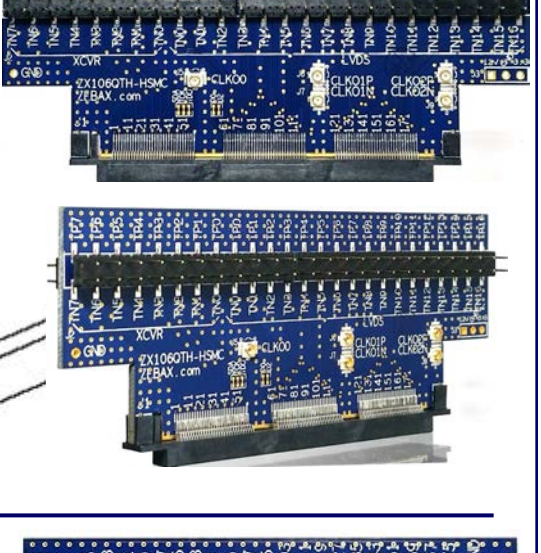
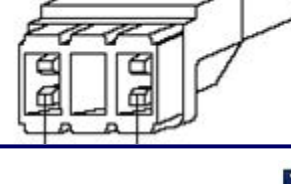
**Application:** Pre-Bringup, bringup, test, debug of Altera HSMC bus interface.



**[ZX106QTH090-HSMC](#)**

[ZX106QTH090-HSMC](#) breakout adapter is designed for test/debugging of Altera HSMC bus interface applications. All HSMC signals are accessible on 0.15” pitch header. Agilent KEYSIGHT E5381A differential probe or any scope probe may be used for monitoring the HSMC signals. Onboard IPEX connectors, enables access clocks to/from Host-daughter card. Designed in 6 layers 100 Ω differential trace impedance.

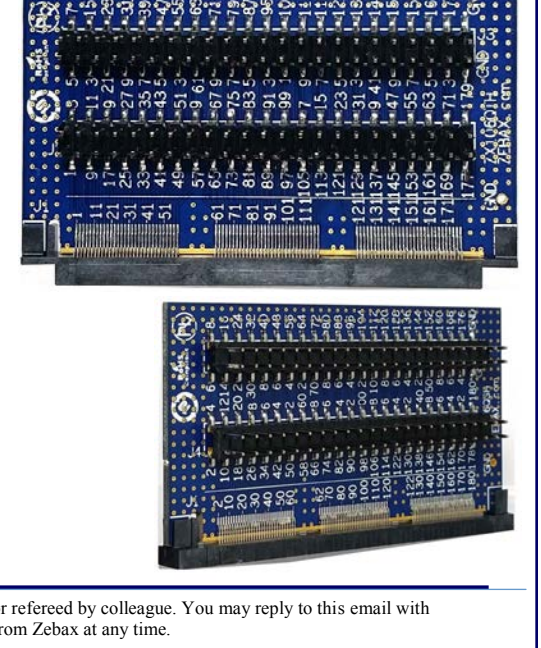
**Application:** Pre-Bringup, bringup, test, debug of Altera HSMC bus interface.



**[ZX106-QTH090-ST](#)    [ZX106-QSH090-ST](#)**

[ZX106-QTH090-ST](#) and [ZX106-QSH090-ST](#) - breakout adapters are general purpose test boards for test/debugging of Altera HSMC bus interface or ANY application using Samtec QSH or QTH 090 connector. All the Samtec signals are accessible on 0.10” pitch header for use with any scope probe. Designed in 6 layers 50 Ω trace impedance.

**Application:** Pre-Bringup, bringup, test, debug of Samtec QSH QTH 090 and Altera HSMC design.



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