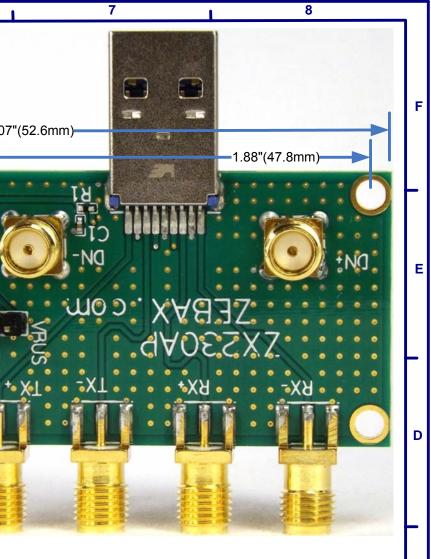
	1	1	2	1	3	1	4	1	5	1	6	7	1	8	
	Product Name:						& Receiver Test		-	oter					
F	Product Description:	High Spe and Rec	eed Inter-Chip I eiver test fixture	HSIC USB 2.0 e (breakout Ad	- SuperSpeed lapter) with th	USB Inter-0 ne USB2.0 U	Chip USB 3.0 test ISB 3.0 Type A co	board Ti nnector	ransmitter			-2.07"(52.6mm)	• =		F
		Designe SCC pro	d meeting elect files tests. Slev	rical complianc v, Voltage level	ce testing; eye ls and more).	diagram, jitte	er (random, deter	ministic,	total jitter,	-			-1.88	'(47.8mm)	
11		Designe	d in 4 layer - 10	00 Ω differential	l trace impedar	nce - electric	cal test measurem	ent requi	rements.		1	18. 			
E		Includes Accessib	: USB3.0 Type ble 0.1" (2.54mr	• A Plug with st m) pitch header	andard SMA c r, enabling "Po	connectors. wer measur	ement" of the VBL	JS.		1.00"(25.4mm)	$\textcircled{\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$		• • • • • •		E
	ZX230 Insertion loss - T Freq. Log Mag (100MHz 0.04775 1.25GHz 0.9045	-dB)								1.20"(30.5mm)		د میں بچر	SEBAX.	7	
	2.5GHz 1.03775 5GHz 3.87325	5	Test Case: Us Network Anal	sing ZX230AP(yzer: Agilent, N	Type A Plug) v Model E5071C	with ZX230A C 4 ch. 8.5GI	AR(type A Recept Hz	acle)				- XT - XT	*X8		
D	Application:		al test, Clock Jit ification full elec		ent, eye diagra	m, charactei	rization test,								D
	SMA –	Brass wit	peed Inter-Chip h Gold finish 5inter, 0.025" SQ	u" on RING, 35	u" GOLD Plate	ed at pin. Ins	r-Chip Specificatio sulator: PTFE	on 3.0 Re	ceptacle		A CONTRACTOR OF			A MAR	
с	Test procedure: Pinouts: USB 2.	·	ible with Agilen 0 Type A Plug		5	·	rocedure		0			1 V		er Function GND VBUS	с
	USB Bandwidth :		elow lists the state and USB 3.0 s			Circuit Diag						3 4 (DP VBUS Powe	r measurements: for power measurement	nts.
-	USB Low Speed USB 2.0 1.5Mb Full Speed USB 2.0 12Mbp					GND	C1 0402 SMD 0.1µF 0402 SMD			mos. XABBZ	2	6 7 (0 8	RX+ GND TX-		$\left \right $
в	High Speed USB 2.0 480MI SuperSpeed USB3.0 5Gbps					Θ 100 Ω =	RX- RX+	USB connector Plug				Note	AX products are RoHS compliant and	I Lead Free unless otherwise indicate	B
-							0 Ω 100 Ω SMA Connectors				A CONTRACTOR OF A	ZEBAX TEC SANTA CRUZ, CA U WWW.ZEBAX.COM			$\left \right $
												SPECIFIED DIMENSIO ARE INCHES (MM). ROHS COMPLIANT		DRAWING	d A
	Notice ALL ZEBAX TECHNOLOGIES DESIGN SPEC IMPLIED, STATUTORY, OR OTHERWISE WI												I SIC USB 2.0 SuperSpe st Board – Type A Plug bi	eed Inter-Chip USB 3. reakout adapter Test Fixture	
	Information furnished is believed to be accurat Specifications mentioned in this publication are	e and reliable. How	wever, Zebax Technologies as	ssumes no responsibility for th	e consequences of use of su	uch information or for an	y infringement of patents or other rig	ghts of third partie	s that may result from its use.			CHECKED: M. MARINA	DRAWN: SLAVIK	REVISSION: 1.0 SHEET: 1 OF 1	11
	1		2	İ	3		4		5	ĺ	6	7		. 8	



Type A Plug				
Function				
VBUS				
DN				
DP				
GND				
RX-				
RX+				
GND				
TX-				
TX+				

Power					
Pin	Function				
1	GND				
2	VBUS				