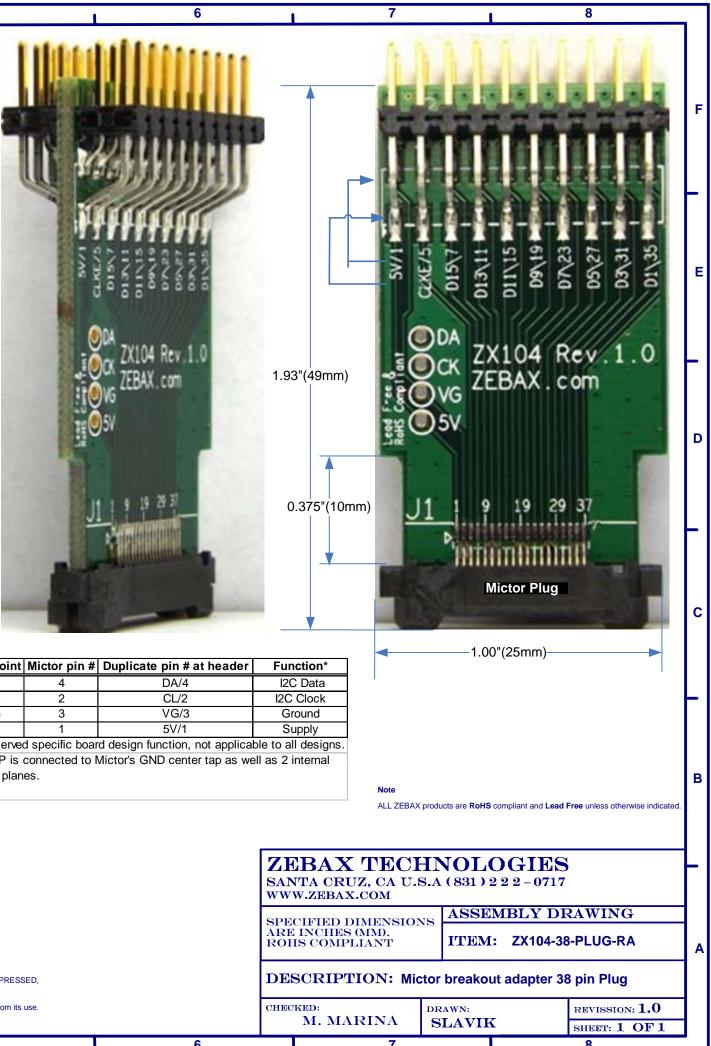
	1		1	2	1	3		4		5			
	Product Nam	ne: Z	X104-38-PLU	G-RA Mictor	Breakout A	dapter				Ĩ			
F	Product Des	Description: Mictor breakout adapter Plug 38 pin Tyco/AMP/TE. Designed in 4 layer PCBoard with improved signal integrity and crosstalk.											
-		In addition to ALL Mictor signal being available at header, <i>dedicated</i> test points for I2C-SDA, I2C-SCL, +5V, GND are provided for direct connection "GND" Header test points are connected to 2 inner layer GND plates <i>along with Mictor's center GND tap</i> . Please use these GNDs as your system GND reference.											
	Application:	Bringup,	testing, emula	tion, developr	ment, modula	ar design eva	aluations, [	DDR qualifica	tion				
E	Mates with :	es with : Any Tyco's 38 pin Mictor Socket offered at any height 2-767004-2 767096-8 767096-1 767110-1 767114-1 767114-8 767181-1 767054-1 767171-1 767081-1 767115-1 2-5767004 5767096-8 5767096-1 5767110-1 5767114-1 5767114-8											
		2-5767004 5767096-8 5767096-1 5767110-1 5767114-1 5767114-8 5767181-1 5767054-1 5767171-1 5767081-1 5767115-1 767130-1 767146-1 767146-8 767094-1 767178-1 767178-8 767154-1 767153-8 767095-1 5767130-1 5767146-1 5767146-8 5767094-1 5767178-1 1761316-8 5767154-1 5767153-8 5767095-1											
D			767044-1 767007 767056 767111 767116 767117 767118 767119 5767006 5767044 Agilent 5346-6002 E5339A E5334A E5351A E5346-6002 E5346-63201										
	Pitch:	0.64mm	(0.025") cente	rline contact s	spacing conn	ector, Legac	y Mictor	ligh Speed co	nnector	- 1			
4	Headers:	0.1" (2.54	4mm) center, (	).025" SQ with	h 0.228 (5.8r	mm) post hei	ght			- 1	2		
	ZX104- Micto	r 38 Plug p	in configurati	on									
	Layer												
с	ТОР	J2	VG 3 5V 1	GND 9	D14 D12 13 D15 D13 11	17 21	D8 D6 25 D9 D7 23	29 33	D2 D0 37 D3 D1 35	Test Poir	nt Mictor pin #		
-	TOP BOTTOM	tor_Plug	J1			<mark>1, 3, 5, 7, 9,</mark> 2, 4, 6, 8, 10,				DA CL VG 5V	4 2 3 1		
в	BOTTOM	J3	DA 4 CH 2	6 8 GND 10	015 013 12 014 012 14	16 20	09 07 24 08 06 26	28 32	03 01 36 02 00 38	"*" Reserv	ed specific boards s connected to M anes.		
_	Header numberin "GND" header pin O0 O15 Odd E D0 D15 Even E CLKO - Clock Oc CLKE - Clock Ev	ns are connec Data bus Data Bus dd Side of Mic	ted to the 2 inter		s as well as top,	/bottom GND fil	ls.						
A	Notice		yntax 5v 1	Header is a	ssigned to Mict								
	ALL ZEBAX TECHNOLOGIE IMPLIED, STATUTORY, OR Information furnished is belie Specifications mentioned in th	OTHERWISE WITH R eved to be accurate and	ESPECT TO THE MATERIA d reliable. However, Zebax T	LS, AND EXPRESSLY DIS	SCLAIMS ALL IMPLIED \ esponsibility for the conse	WARRANTIES OF NO IN equences of use of such	FRINGEMENT, MEF	RCHANTABILITY, AND FITM y infringement of patents or	NESS FOR A PARTIC	ULAR PURPOSE.			



Test Point	Mictor pin #	Duplicate pin # at header	Fu		
DA	4	DA/4	12		
CL	2	CL/2	120		
VG	3	VG/3	0		
5V	1	5V/1	9		
*" Reserved specific board design function, not applicable					

