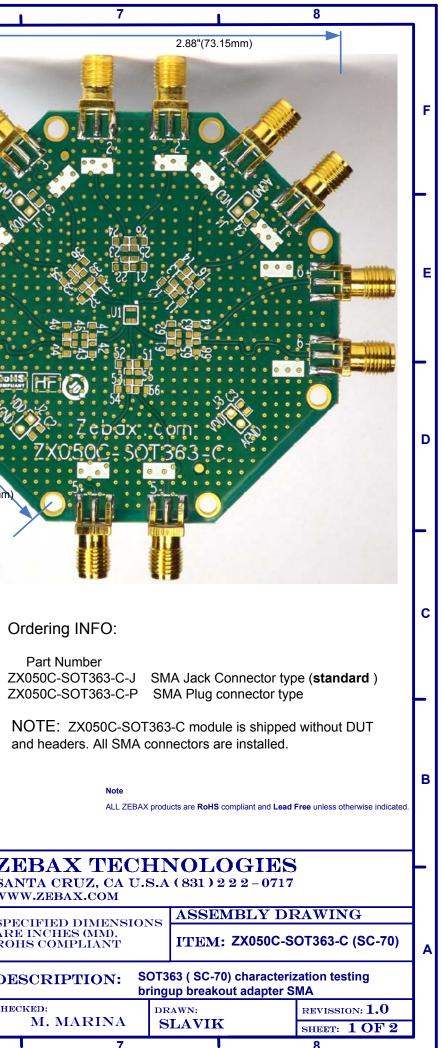
	1	2	3	4		5	6
	Product Name:	ZX050C-SOT363-C (SC-70	) SOT363 Characterizatio	n testing bringup b	reakout module		
F	Product Description:	High Speed characterization modu U1, is accessible as described belo	le meeting 10GHz signal bandwid	dth with < 0.3dB insertior	n loss. Each pin of the	device under test- DU	r,
		<ol> <li>Selectable stuffing option servin</li> <li>Two dedicated SMA connectors</li> <li>ZX50C-SOT363-C is designed v</li> <li>Designed with 1oz copper ensur</li> </ol>	for each ASIC pin, U1, 1- Extern with 50 $\Omega$ ( Ohms ) trace impedar	al stimulus 2- Measuren ce using 4 layers PCB e	nsuring PCB+Connect	or insertion loss <0.3dl z copper.	3
		Ideal for ASIC Integrated Circuit ( I MOSFET, Load Switch, LDO, and	more, using SOT23 footprint pac	kage.			
E		Available 2 pin headers ( with asso Capacitors, C1, C2, C3 and C4 are	e the headers' associated decoup	ling capacitors – 0.1 μF	or similar.	J2, J3 and J4). The	
	Ameliantian	AGND pin of the 2 pin headers is t			e GND plane.	1	1.09"(27.6mm)
	Application: Target DUT :	Bringup, Characterization, testing, Designed specifically for any SOT the SOT363 device.			III pins of		
D	Pitch:	Standard SOT363 (SC-70 ) SMD p	backage or equivalent			Block Diagram: See Page 2	100
	Headers:	2 pin test point (VDD - GND) - 0.02	25" SQ with 0.32" (5.6mm) post h	eight		Ū	2.88"(73.15mm)
	DUT landing pads:	Surface Mount, 3 pin package – Bo	ody 2.9x1.3mm (Length Width) o	or equivalent			1.09"(27.6mm)
17	<b>SMA:</b> Impedance: 50Ω	DIMENTI	ONS ( mm)				
с	Temp Range: -65°C -	D-202, Method 213 mm T	Z G max ypical 2.50 1.30 min		1 C2 90 0.65		
	Working Voltage: Withstand Voltage: Center Contact:	335V max 1000V rms ≤3mΩ	REER	ENCES JEITA	<b>←</b> C2 <b>→</b>		
	Outer Contact: Insulation resistance: VSWR Straight:		T323	SC-70			JACK (J)
в							
					<u>↓ ↓</u> <u> </u>   ↓ × ▶		Z
							SAI WW
A	Notice						ARI RO
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l L	4	2	3	4		5	6



	1	1	2	3	4	1	5	6	
	Product Name:	ZX050C-SOT	363-C (SC-70) SOT:	363 Characterization	testing bringup br	eakout module			
	DUT Application Spe	ecific Stuffing o	ptions, DASS:						
F				Specific Stuffing, DASS,	componente comprisina	of six 0603 SMD packag	e devices		
_		SMA11 "1+"	DASS component block		g	o. o o o o o o o o o o o o o o o o o			
E									
		2. RC12 is referenced as U1.Pin1, 2 <sup>26</sup> the RC13 and RC15 are RECUIRED for 3. "1." and "1+" are silkscreen at the SM "1." is referenced to RC11 since it "1+" is referenced to RC12 since it DASS: DUT Application Specific Stuffing	A connectors referenced to the U1.pin1. references to GND. t references to VDD.	or etc ferred as DASS) where					
		General 2pin header, description: VDD and AGND: 1- Available onboard standard 0.1"							-
		<ul> <li>2- VDD pin is connected to the VDD</li> <li>3- AGND is connected to the AGND</li> <li>4- Each header is accompanied by the second secon</li></ul>	) PCB plane - 3 <sup>rd</sup> layer ) PCB plane - 2 <sup>rd</sup> layer 0603 SMD package for 0.1uF decoupling capacitor, if required	1.					
D	Stuffing option, Matr		ovides suggested stuffing UT, supply and the GND		imponents for push-pull,	and open drain devices f	or		
	DUT Pin	Ctuffing ontions	push-pu	II devices	open dra	in devices	]		
	configuration	Stuffing options	Low voltage devices	High Voltage devices	Low voltage devices	High Voltage devices			R
	Supply, VDD	RCx2	0.1 µF	0.1 μF	0.1 µF	0.1 µF			SMA32
	Supply GND	RCx1	0 Ω	0 Ω	0 Ω	0 Ω			
	INPUT	RCx1	50 Ω	50 Ω	50 Ω	50 Ω			
С		RCx1	5 pF	40 pF	5 pF	40 pF			
	OUTPUT	RCx5	1050 Ω	453 Ω	1050 Ω	453 Ω			
		RCx6		d open	2Κ Ω	499 Ω			
	Application Specific	RCx3, RCx5		on Specific Suffing ( DAS	S) - Must install with 0	Ω, if Not used.			
в	Note: Each pin of U1 na	as identical stuming o	ptions. RCx1 where the x	t is the U1.pin number					ZF
									8 

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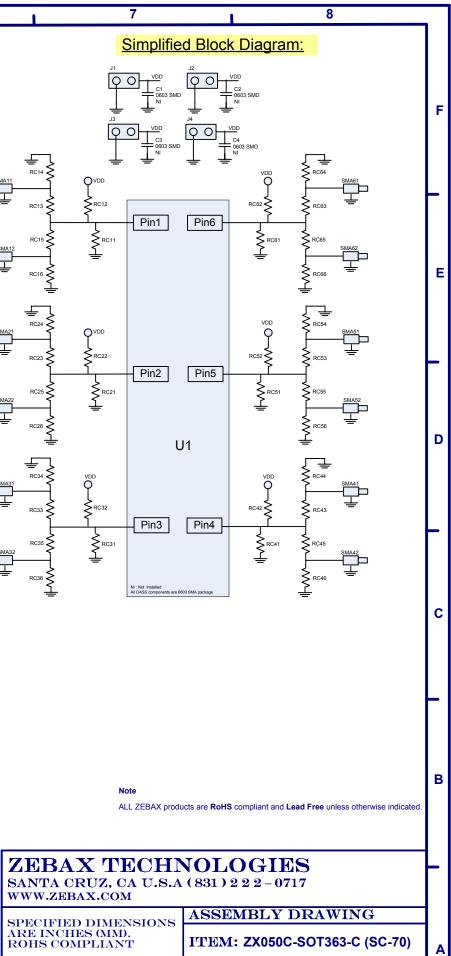
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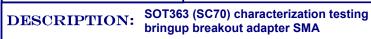
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2





6

DESCRIPTION: bringup breakout adapter SMA					
CHECKED:	DRAWN:	REVISSION: 1.0			
M. MARINA	SLAVIK	SHEET: 2 OF 2			
7		8			